

What Is Claimed Is:

1. A liquid crystal display device for removing residual charge, comprising:
 - a plurality of data lines and a plurality of gate lines arranged along vertical and horizontal directions, respectively, on a transparent substrate;
 - a source driver for supplying data voltage to the data lines;
 - a gate driver for supplying gate voltage to the gate lines; and
 - a plurality of common voltage lines connected to the data lines and the gate lines through a plurality of static electricity preventing units,
 - wherein at least one of the static electricity preventing units is directly connected to the source driver.
2. The device according to claim 1, wherein the common voltage lines receive a common voltage from the gate driver.
3. The device according to claim 1, further comprising a first dummy line connected to the gate lines and the common voltage lines through the static electricity preventing units.

4. The device according to claim 3, further comprising a second dummy line connected to the data lines and the common voltage lines through the static electricity preventing units.
5. The device according to claim 1, wherein the gate driver is electrically connected to the source driver to receive a gate signal and a control signal.
6. The device according to claim 1, wherein the common voltage lines include at least one silver dot.
7. The device according to claim 1, further comprising a source printed circuit board for supplying a gate signal, a data signal, and a control signal to the source driver.
8. A liquid crystal display device for removing residual charge, comprising:
 - a plurality of data lines and a plurality of gate lines arranged along vertical and horizontal directions, respectively, on a transparent substrate;
 - a source driver for supplying data voltage to the data lines;
 - a source printed circuit board for supplying a gate signal, a data signal, and a control signal to the source driver;

a gate driver for supplying gate voltage to the gate lines; and
a plurality of common voltage lines connected to the data lines and the gate lines through a plurality of static electricity preventing units,
wherein at least one pathway of the residual charge flows to ground directly through the source driver and the source printed circuit board.

9. The device according to claim 8, wherein the common voltage lines receive a common voltage from the gate driver.

10. The device according to claim 8, further comprising a first dummy line connected to the gate lines and the common voltage lines through the static electricity preventing units.

11. The device according to claim 10, further comprising a second dummy line connected to the data lines and the common voltage lines through the static electricity preventing units.

12. The device according to claim 8, wherein the gate driver is electrically connected to the source driver to receive a gate signal and a control signal.

13. The device according to claim 8, wherein the common voltage lines include at least one silver dot.

14. A liquid crystal display device for removing residual charge, comprising:

a plurality of data lines and a plurality of gate lines arranged along vertical and horizontal directions, respectively, on a transparent substrate;

a source driver for supplying data voltage to the data lines;

a source printed circuit board for supplying a gate signal, a data signal, and a control signal to the source driver;

a gate driver for supplying gate voltage to the gate lines; and

a plurality of common voltage lines connected to the data lines and the gate lines through a plurality of static electricity preventing units,

wherein at least one pathway of the residual charge flows to ground through driver circuitry of the source driver and the source printed circuit board.

15. The device according to claim 14, wherein the common voltage lines receive a common voltage from the gate driver.

16. The device according to claim 14, further comprising a first dummy line connected to the gate lines and the common voltage lines through the static electricity preventing units.

17. The device according to claim 16, further comprising a second dummy line connected to the data lines and the common voltage lines through the static electricity preventing units.

18. The device according to claim 14, wherein the gate driver is electrically connected to the source driver to receive a gate signal and a control signal.

19. The device according to claim 14, wherein the common voltage lines include at least one silver dot.

20. A method for removing residual charge from a liquid crystal display device, comprising:

providing a first dummy line connected to each one of a plurality of gate lines;

providing a second dummy line connected to each of a plurality of data lines;

providing a plurality of static electricity preventing units between each of the gate lines and the first dummy line, between each of the data lines and the second dummy line, and between the first and second dummy lines;

providing a source driver for supplying data voltage to the data lines;

providing a source printed circuit board for supplying voltage signals to the source driver;

providing a gate driver for supplying gate voltage to the gate lines; and

providing a plurality of common voltage lines connected to the data lines and the gate lines through the plurality of static electricity preventing units,

wherein at least one pathway of the residual charge flows to ground from the first dummy line through the source driver and the source printed circuit board.

21. The method according to claim 20, wherein the pathway includes drive circuitry of the source driver.

22. The method according to claim 20, wherein the pathway excludes drive circuitry of the source driver.

23. A method for removing residual charge from a liquid crystal display device, comprising:

providing a liquid crystal panel, the liquid crystal panel including:

a plurality of data lines and a plurality of gate lines arranged along vertical and horizontal directions, respectively, on a transparent substrate;

a source driver for supplying data voltage to the data lines;

a source printed circuit board for supplying a gate signal, a data signal, and a control signal to the source driver;

a gate driver for supplying gate voltage to the gate lines; and

a plurality of common voltage lines connected to the data lines and the gate lines through a plurality of static electricity preventing units, connecting at least one pathway to ground through the source driver and the source printed circuit board; and

discharging the residual charge through the pathway.

24. The method according to claim 23, wherein the pathway includes drive circuitry of the source driver.

25. The method according to claim 23, wherein the pathway excludes drive circuitry of the source driver.